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AMENDMENTS TO THE CLAIMS

The listing below of the claims will replace all prior versions and listings of claims in the present application:

Listing of Claims:

Claim 1 (previously presented): A method for a wireless transmission of data between one computer and at least one other computer with the aid of a digital transmission system for the wireless transmission of digital data, where a transmitting computer is connected to a digital transmitter and where a receiving computer is connected to a respective digital receiver, said method comprising the steps of: intermittently transmitting digital information from the transmitting computer to a first adaptation circuit; storing information received from the transmitting computer for transmission to the receiving computer in a first memory associated with the first adaptation circuit, wherein the first adaptation circuit is disposed between the transmitting computer and the digital transmitter; substantially continuously outputting information from said first memory to said digital transmitter under a control of an outfeed oscillator in the first adaptation circuit; substantially continuously transmitting digital information from the digital transmitter to a digital receiver operatively coupled with the receiving computer; feeding the substantially continuously digitally transmitted information from the digital receiver into a second memory associated with a second adaptation circuit disposed between the digital receiver and the receiving computer and that is under a control of an infeed oscillator in the second

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adaptation circuit; storing the substantially continuously digitally transmitted information in the second memory; operating the two oscillators at substantially the same frequency; and conveying intermittently from the second memory in the second adaptation circuit to the receiving computer information that has been received from the digital receiver and stored in the second memory.

Claim 2 (previously presented): A method according to Claim 1, including the step of synchronizing the frequency of the infeed oscillator in the second adaptation circuit with the frequency of the outfeed oscillator in the first adaptation circuit by locking the frequency of the infeed oscillator onto a reference included in the digitally transmitted signal from the digital transmitter.

Claim 3 (previously presented): A method according to Claim 1, including the steps of providing in the second adaptation circuit a microprocessor for determining from a fast information channel (FIC) in the digital system those parts of the digitally transmitted signal that contain data, and storing the digitally transmitted data in the second memory.

Claim 4 (previously presented): A method according to Claim 3, including the step of identifying in the microprocessor of the second adaptation circuit information that is relevant to a receiving computer and that includes identification of address information.

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Claim 5 (previously presented): An arrangement for a wireless transmission of data between a first computer and at least one second computer with the aid of a digital transmission system for the wireless digital transmission of data, said arrangement comprising: a transmitting computer connected to a digital transmitter; a receiving computer connected to a digital receiver; a first adaptation circuit disposed between the transmitting computer and the digital transmitter, said first adaptation circuit adapted to store information delivered intermittently from the transmitting computer in a first memory associated with said first adaptation circuit, wherein the first adaptation circuit outputs the information from said first memory to said digital transmitter substantially continuously; an outfeed oscillator disposed in the first adaptation circuit; a second adaptation circuit disposed between the digital receiver and the receiving computer; a second memory disposed in the second adaptation circuit, wherein said second adaptation circuit inputs information substantially continuously received by the digital receiver into the second memory; an infeed oscillator disposed in said second adaptation circuit for controlling the transmission of information from the second memory to the receiving computer, wherein the outfeed and infeed oscillators operate at substantially the same frequency; and wherein the receiving computer receives information intermittently from the second memory.

Claim 6 (previously presented): An arrangement according to Claim 5, wherein the frequency of the infeed oscillator in the second adaptation

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circuit is synchronized with the frequency of the outfeed oscillator in the first adaptation circuit by locking the frequency of the infeed oscillator to a reference signal included in the transmitted signal.

Claim 7 (previously presented): An arrangement according to Claim 5, wherein the second adaptation circuit includes a microprocessor for determining from a fast information channel (FIC) in the digital system which parts of the transmitted signal contain data, and to store transmitted data in the second memory.

Claim 8 (previously presented): An arrangement according to Claim 7, wherein the microprocessor in the second adaptation circuit identifies transmitted information that is relevant to the receiving computer and that includes identification of address information.